

**REMARKS**

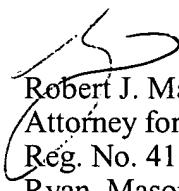
This Preliminary Amendment is submitted to correct errors in FIGS. 21 and 25 and corresponding errors in the disclosure. No new matter is introduced. This Preliminary Amendment is submitted pursuant to 37 C.F.R. 1.115 before the first examination and is respectfully requested to be entered prior to substantive examination of the claims. Applicants have submitted marked up and clean versions of the drawings. Additionally, as the corrections are of a typographical nature, Applicants have previously submitted formal drawings containing the corrections.

In FIG. 21, the number "1904" should have been --1994--. Gate 1994 is shown in FIGS. 19 and 20, and FIG. 21 should also have gate 1994. Corresponding changes have been made to the specification. In FIG. 25, "2T1D" has been changed to --3T1D--, in accordance with the memory cells shown in FIG. 25, which are three transistor (3T), one diode (1D) cells.

If any outstanding issues remain, or if the Examiner has any further suggestions for expediting allowance of this application, the Examiner is invited to contact the undersigned at the telephone number indicated below.

The Examiner's attention to this matter is greatly appreciated.

Respectfully submitted,

  
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Date: March 12, 2004

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